

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10033123	FILING DATE 10/22/2001	CLASS 712	SUBCLASS 2133	GAU 686 2133	EXAMINER Hai L. NGUYEN
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\*\*CONTINUING DATA VERIFIED:

None HLN

\*\* FOREIGN APPLICATIONS VERIFIED:

GERMANY 100 52 210.6 10/20/2000 HLN

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no HLN	ATTORNEY DOCKET NO P2000,0257
TITLE : Integrated circuit having a synchronous and an asynchronous circuit and method for operating such an integrated circuit		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims 7	Print Claim for O.G 1
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg. 2	Figs.Drwg. 2
TERMINAL DISCLAIMER		Print Fig. 1	
		Primary Examiner	
		PREPARED FOR ISSUE	
		Application Examiner	
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